



U.S. UTILITY Patent Application

O.I.P.E.	<i>H54</i>	PATENT DATE
SCANNED	GTH(4)	AUG 21 2001
Q.A.		<i>CS4</i>

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/498297		323	282	2838	<i>Patel</i>

APPLICANTS
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TITLE
Flip-chip switching regulator.

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
323	282	257	700		
INTERNATIONAL CLASSIFICATION					
G05F	1/40				
H01L	23/053				
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

<input type="checkbox"/> TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
		<i>8/11</i>	<i>19</i>	<i>2</i>	<i>43</i>	<i>1</i>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED	
					<i>2-27-01</i>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____					ISSUE FEE	
					Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.		<i>Rajivant D. Patel</i> <i>(Primary Examiner)</i>			<i>\$620.00</i>	<i>5/14/01</i>
						ISSUE BATCH NUMBER
						<i>C67</i>

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ISSUE FEE IN FILE

Formal Drawings (shts) set

(FACE)

ISSUE FEE